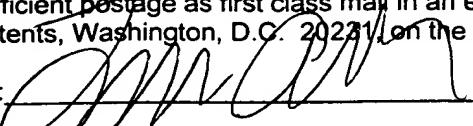


Docket No.: GR 98 P 2314 P

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By: 

Date: July 11, 2002

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor : Siegfried Schwarzl  
Applic. No. : 09/781,173  
Filed : February 12, 2001  
Title : Memory Cell Array And Method For Manufacturing It  
Examiner : Anh K. Phung - Art Unit: 2824

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**RESPONSE**

Hon. Commissioner of Patents and Trademarks,  
Washington, D. C. 20231

Sir:

Responsive to the Office Action dated June 19, 2002, the following remarks are made:

8/7/02

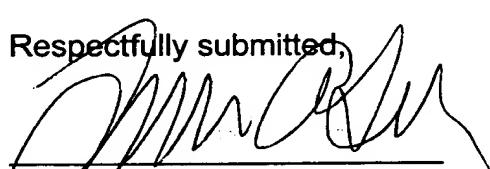
In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 1-9, for prosecution at this time.

OBrown

Applicants reserve the right to file a divisional application including the method claims of group II at a later date and/or to request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-9 is solicited.

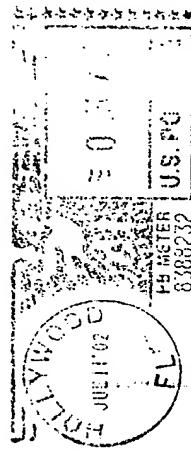
Respectfully submitted,

  
For Applicant

LAURENCE A. GREENBERG  
REG. NO. 29,308

Date: July 11, 2002

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